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APPLICATION NO.	FILING DATE	FIRST NAMED INVEN	TOR		ATTORNEY DOCKET NO.
09/224,029	12/31/98	DIMARCO		M	A62-25127-US
_			$\neg$	EXAMINER	
		MM91/0515			
JOHN G SHU	DY JR			DINH.	T
HONEYWELL	INC OFFICE	OF GENERAL COUNSE		ART UNIT	PAPER NUMBER
HONEYWELL		8251			a
P 0 BOX 52				2841	7
	- S MN 55440-	0524		DATE MAILED:	
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Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 

	Application No.	Applicant(s)					
Advisory Action	09/224,029	DIMARCO, MARIO					
-	Examiner	Art Unit					
	Tuan T Dinh	2841					
The MAILING DATE of this communication appe							
THE REPLY FILED 07 May 2001 FAILS TO PLACE THI Therefore, further action by the applicant is required to avinal rejection under 37 CFR 1.113 may only be either: (1 condition for allowance; (2) a timely filed Notice of Appea Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this applicat ) a timely filed amendment which	ion. A proper reply to a places the application in					
PERIOD FOR R	EPLY [check only a) or b)]						
<ul> <li>a)</li></ul>	n two months as set forth in MPEP § 706. R continues to run from the mailing date o	of the final rejection,					
Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Officimely filed, may reduce any earned patent term adjustment. See 37 C	of extension and the corresponding amou the shortened statutory period for reply o ce later than three months after the maili	nt of the fee. The appropriate extension riginally set in the final Office action; or					
<ol> <li>A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37CFF</li> </ol>	s Brief must be filed within the per R 1.191(d)), to avoid dismissal of t	iod set forth in the appeal.					
<ol> <li>The proposed amendment(s) will be entered upon with requisite fees.</li> </ol>	the timely submission of a Notice	e of Appeal and Appeal Brief					
3. The proposed amendment(s) will not be entered b	ecause:						
(a) ☐ they raise new issues that would require further consideration and/or search. (see NOTE below);							
(b) ☐ they raise the issue of new matter. (see Note below);							
(c)  they are not deemed to place the application i issues for appeal; and/or							
(d) they present additional claims without cancel	ing a corresponding number of fir	nally rejected claims.					
NOTE: <u>See Continuation Sheet</u> .							
<ol><li>Applicant's reply has overcome the following rejecti</li></ol>	on(s):						
5. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	parate, timely filed amendment					
6. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because:	reconsideration has been consider—.	lered but does NOT place the					
7. The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	cause it is not directed SOLELY to	o issues which were newly					
8. $\square$ For purposes of Appeal, the status of the claim(s)	is as follows (see attached writter	n explanation, if any):					
Claim(s) allowed:							
Claim(s) objected to:							
Claim(s) rejected:							
Claim(s) withdrawn from consideration:							
9. The proposed drawing correction filed on a	ı)∏has b)∏ has not been appro	oved by the Examiner.					
10. Note the attached Information Disclosure Stateme	ent(s)( PTO-1449) Paper No(s)	H-1/6/1/1/					
11. Other:							





Application NO.

Continuation of 3. NOTE: Craker still meets all the limitations of the claimed invention. Basked on figure 1 of Craker, it clearly teaches "a connector assembly coupled to each of said first circuit board (column 2, lines 38-41) and inherently having a connector assembly coupled to said second circuit board". It also clear teaches "said connector assembly is configured to provide an electrical interface between said first and second circuit boards and said cabinet (column 2, lines 45-49)".